SIEMENS

Data sheet

6ES7212-1AF40-0XB0



SIMATIC S7-1200F, CPU 1212 FC, compact CPU, DC/DC/DC, onboard I/O: 8 DI 24 V DC; 6 DO 24 V DC; 2 AI 0-10 V DC, Power supply: DC 20.4-28.8V DC, Program/data memory 100 KB

CPU 1212FC DC/DC/DC
V4.5
STEP 7 V17 or higher
Yes
20.4 V
28.8 V
Yes
24 V
20.4 V
28.8 V
400 mA; CPU only
1 200 mA; CPU with all expansion modules
12 A; at 28.8 V DC
0.5 A²-s
1 000 mA; Max. 5 V DC for SM and CM
L+ minus 4 V DC min.
9 W
100 kbyte
No
2 Mbyte
with SIMATIC memory card
Yes
Yes

for bit operations, typ. for bit operations, typ. for floating point arithmetic, typ. CPU-blocks Number of blocks (total) BBs, FCs, FBs, counters and timers. The maximum number of addressable blocks ranges from 1 to 65535. There is no restriction entire working memory can be used OB Number, max. Limited only by RAM for code Data areas and their retentivity Retentive data area (incl. timers, counters, flags), max. Flag Size, max. Local data per priority class, max. Address area Process image Inputs, adjustable Outputs, adjustable Audivare configuration Number of modules per system, max. Time of day Clock Hardware clock (real-time) Backup time Backup time Deviation per day, max. Digital inputs Number of digital inputs Num	
for word operations, typ. for floating point arithmetic, typ. CPU-blocks Number of blocks (total) BBs, FCs, FBs, counters and timers. The maximum number of addressable blocks ranges from 1 to 65535. There is no restrictive nentire working memory can be used Because and their retentivity Retentive data area (incl. timers, counters, flags), max. Flag Size, max. Local data per priority class, max. Local data per priority class, max. Address area Process image Inputs, adjustable Outputs, adjustable Audiverse configuration Number of modules per system, max. Time of day Clock Hardware clock (real-time) Backup time Deviation per day, max. Number of digital inputs of which inputs usable for technological functions 8; Integrated 4; HSC (High Speed Counting)	
for floating point arithmetic, typ. CPU-blocks Number of blocks (total) DBs, FCs, FBs, counters and timers. The maximum number of addressable blocks ranges from 1 to 65535. There is no restrictic entire working memory can be used OB Number, max. Limited only by RAM for code Data areas and their retentivity Retentive data area (incl. timers, counters, flags), max. Flag Size, max. Local data per priority class, max. 16 kbyte; Priority class 1 (program cycle): 16 KB, priority class 2 KB Address area Process image Inputs, adjustable Outputs, adjustable Outputs, adjustable Address or indules per system, max. Time of day Clock Hardware clock (real-time) Backup time Deviation per day, max. Process image As on m. modules, 1 signal board, 2 signal modules Time of day Clock Hardware clock (real-time) Backup time Deviation per day, max. Backup time Deviation per day, max. Backup time Backup tim	
CPU-blocks Number of blocks (total) DBs, FCs, FBs, counters and timers. The maximum number of addressable blocks ranges from 1 to 65535. There is no restrictic entire working memory can be used Number, max. Limited only by RAM for code Data areas and their retentivity Retentive data area (incl. timers, counters, flags), max. Flag Size, max. Local data per priority class, max. Local data per priority class, max. Local data process image Inputs, adjustable Outputs, adjustable Outputs, adjustable Hardware configuration Number of modules per system, max. Time of day Clock Hardware clock (real-time) Backup time Deviation per day, max. Number of digital inputs Obey Alone (real-time) Backup time Deviation per day, max. Number of digital inputs Number of digital inputs Obey Alone (Alone) Sintegrated Ago h; Typical Obey Alone (Alone) Sintegrated Ago h; Horgrated Ago h; Ho	
Number of blocks (total) DBs, FCs, FBs, counters and timers. The maximum number of addressable blocks ranges from 1 to 65535. There is no restrictic entire working memory can be used Number, max. Limited only by RAM for code Limited only by RAM for code Pata areas and their retentivity Retentive data area (incl. timers, counters, flags), max. Flag Size, max. Local data per priority class, max. 16 kbyte; Size of bit memory address area Local data per priority class, max. 16 kbyte; Priority class 1 (program cycle): 16 KB, priority class 2 KB Address area Process image Inputs, adjustable Outputs, adjustable Outputs, adjustable Outputs, adjustable Address area 1 kbyte Hardware configuration Number of modules per system, max. 3 comm. modules, 1 signal board, 2 signal modules Time of day Clock Hardware clock (real-time) Backup time Deviation per day, max. 480 h; Typical So s/month at 25 °C Digital inputs Number of digital inputs of which inputs usable for technological functions 8; Integrated 4; HSC (High Speed Counting)	
● Number, max. Data areas and their retentivity Retentive data area (incl. timers, counters, flags), max. Flag ● Size, max. Local data ● per priority class, max. Address area Process image ● Inputs, adjustable ● Outputs, adjustable ● Outputs, adjustable 1 kbyte Hardware configuration Number of modules per system, max. Time of day Clock ● Hardware clock (real-time) ● Backup time ● Deviation per day, max. Limited only by RAM for code 14 kbyte 14 kbyte 16 kbyte; Priority class 1 (program cycle): 16 KB, priority class 2 KB 1 kbyte 1 kbyte 1 kbyte 1 kbyte 3 comm. modules, 1 signal board, 2 signal modules 1 kbyte 60 s/month at 25 °C Digital inputs Number of digital inputs Number of digital inputs 1 kbyte 48 lntegrated 4; HSC (High Speed Counting)	to 26: 6
Data areas and their retentivity Retentive data area (incl. timers, counters, flags), max. Flag • Size, max. Local data • per priority class, max. Address area Process image • Inputs, adjustable • Outputs, adjustable • Outputs, adjustable • Tkbyte Hardware configuration Number of modules per system, max. Time of day Clock • Hardware clock (real-time) • Backup time • Deviation per day, max. Digital inputs Number of digital inputs • of which inputs usable for technological functions 14 kbyte 4 kbyte; Size of bit memory address area 16 kbyte; Priority class 1 (program cycle): 16 KB, priority class 2 KB 16 kbyte; Priority class 1 (program cycle): 16 KB, priority class 2 KB 18 kbyte 1 kbyte 1 kbyte 1 kbyte 1 kbyte 1 kbyte 4 kbyte; Priority class 1 (program cycle): 16 KB, priority class 2 KB 16 kbyte; Priority class 1 (program cycle): 16 KB, priority class 2 KB 16 kbyte; Priority class 1 (program cycle): 16 KB, priority class 2 KB 16 kbyte; Priority class 1 (program cycle): 16 KB, priority class 2 KB 16 kbyte; Priority class 1 (program cycle): 16 KB, priority class 2 KB 16 kbyte; Priority class 1 (program cycle): 16 KB, priority class 2 KB 16 kbyte; Priority class 1 (program cycle): 16 KB, priority class 2 KB 18 kbyte 19 kbyte 1 kbyte	to 26: 6
Retentive data area (incl. timers, counters, flags), max. Flag Size, max. 4 kbyte; Size of bit memory address area Local data per priority class, max. 16 kbyte; Priority class 1 (program cycle): 16 KB, priority class 2 KB Address area Process image Inputs, adjustable Outputs, adjustable Outputs, adjustable I kbyte Address orea Process image Inputs, adjustable Address area Process image Address area Process image Inputs, adjustable Address area Address area Process image Address area Process image Address area Address area Address area Process image A kbyte; Size of bit memory address area Local data A kbyte; Size of bit memory address area Local data Flow byte; Priority class 1 (program cycle): 16 KB, priority class 2 KB Address area Process image A kbyte; Priority class 1 (program cycle): 16 KB, priority class 2 KB Address area Process image A kbyte; Priority class 1 (program cycle): 16 KB, priority class 2 KB Address area Process image A kbyte; Priority class 1 (program cycle): 16 KB, priority class 2 KB	to 26: 6
Flag Size, max. 4 kbyte; Size of bit memory address area Local data per priority class, max. 16 kbyte; Priority class 1 (program cycle): 16 KB, priority class 2 KB Address area Process image Inputs, adjustable Outputs, adjustable Address configuration Number of modules per system, max. 1 comm. modules, 1 signal board, 2 signal modules Time of day Clock Hardware clock (real-time) Backup time Backup time Deviation per day, max. Number of digital inputs Size of bit memory address area 4 kbyte; Priority class 1 (program cycle): 16 KB, priority class 2 KB 1 kbyte 1 k	to 26: 6
Size, max. 4 kbyte; Size of bit memory address area Local data • per priority class, max. 16 kbyte; Priority class 1 (program cycle): 16 KB, priority class 2 KB Address area Process image • Inputs, adjustable • Outputs, adjustable • Outputs, adjustable 1 kbyte Hardware configuration Number of modules per system, max. 3 comm. modules, 1 signal board, 2 signal modules Time of day Clock • Hardware clock (real-time) • Backup time • Deviation per day, max. Pyes 480 h; Typical • Deviation per day, max. Digital inputs • of which inputs usable for technological functions 8; Integrated 4; HSC (High Speed Counting)	to 26: 6
Local data	to 26: 6
Per priority class, max. 16 kbyte; Priority class 1 (program cycle): 16 KB, priority class 2 KB Address area Process image Inputs, adjustable Outputs, adjustable 1 kbyte Hardware configuration Number of modules per system, max. 3 comm. modules, 1 signal board, 2 signal modules Time of day Clock Hardware clock (real-time) Backup time Deviation per day, max. Process image 1 kbyte 1 kbyte 1 kbyte 4 kbyte 4 kbyte 4 signal board, 2 signal modules Time of day Clock Hardware clock (real-time) Sample of system of the signal board, 2 signal modules Time of day Clock Hardware clock (real-time) Backup time Obeviation per day, max. Sample of system of the signal board, 2 signal modules Time of day Clock Hardware clock (real-time) Sample of system of the signal board, 2 signal modules Time of day Clock Hardware clock (real-time) Sample of system of the signal board, 2 signal modules Time of day Clock Hardware clock (real-time) Sample of system of the signal board, 2 signal modules Time of day Clock Hardware clock (real-time) Sample of system of the signal board, 2 signal modules Time of day Clock Hardware clock (real-time) Sample of day Hardware clock (real-time) Sample of signal board, 2 signal modules Time of day Clock Hardware clock (real-time) Sample of day Hardware clock (real-time)	to 26: 6
Address area Process image Inputs, adjustable Outputs, adjustable It kbyte Hardware configuration Number of modules per system, max. Time of day Clock Hardware clock (real-time) Backup time Backup time Deviation per day, max. Digital inputs Number of digital inputs Number of digital inputs Number of digital inputs Number of digital inputs usable for technological functions KB KB KB KB KB KB KB KB Address area I kbyte 1 kbyte	to 26: 6
Process image Inputs, adjustable Outputs, adjustable I kbyte Hardware configuration Number of modules per system, max. 3 comm. modules, 1 signal board, 2 signal modules Time of day Clock Hardware clock (real-time) Backup time Backup time Deviation per day, max. Process image 1 kbyte	
 Inputs, adjustable Outputs, adjustable Hardware configuration Number of modules per system, max. 3 comm. modules, 1 signal board, 2 signal modules Time of day Clock Hardware clock (real-time) Backup time Deviation per day, max. Digital inputs Number of digital inputs usable for technological functions 1 kbyte 60 s/month at 2 signal board, 2 signal modules 480 h; Typical 60 s/month at 25 °C Digital inputs 8; Integrated 4; HSC (High Speed Counting) 	
 Outputs, adjustable Hardware configuration Number of modules per system, max. 3 comm. modules, 1 signal board, 2 signal modules Time of day Clock Hardware clock (real-time) Backup time Deviation per day, max. Particular by per day Bigital inputs Number of digital inputs Integrated of which inputs usable for technological functions HSC (High Speed Counting) 	
Hardware configuration Number of modules per system, max. Time of day Clock Hardware clock (real-time) Backup time Deviation per day, max. Polyiation per day, max. Number of digital inputs Number of digital inputs Number of which inputs usable for technological functions 3 comm. modules, 1 signal board, 2 signal modules Yes 480 h; Typical 60 s/month at 25 °C Digital inputs 8; Integrated 4; HSC (High Speed Counting)	
Number of modules per system, max. 3 comm. modules, 1 signal board, 2 signal modules Time of day Clock Hardware clock (real-time) Backup time Deviation per day, max. Position per day, max. Number of digital inputs Number of digital inputs Number of which inputs usable for technological functions 3 comm. modules, 1 signal board, 2 signal modules Yes 480 h; Typical 60 s/month at 25 °C Digital inputs 8; Integrated 4; HSC (High Speed Counting)	
Time of day Clock	
Clock • Hardware clock (real-time) • Backup time • Deviation per day, max. Clock • Backup time • Deviation per day, max. • Deviation per day, max. • Digital inputs Number of digital inputs • of which inputs usable for technological functions • of which inputs usable for technological functions • Of which inputs usable for technological functions	
Clock • Hardware clock (real-time) • Backup time • Deviation per day, max. Clock • Backup time • Deviation per day, max. • Deviation per day, max. • Digital inputs Number of digital inputs • of which inputs usable for technological functions • of which inputs usable for technological functions • Of which inputs usable for technological functions	
 Backup time Deviation per day, max. S/month at 25 °C Digital inputs Integrated of which inputs usable for technological functions HSC (High Speed Counting) 	
 Backup time Deviation per day, max. S/month at 25 °C Digital inputs Integrated of which inputs usable for technological functions HSC (High Speed Counting) 	
 Deviation per day, max. 60 s/month at 25 °C Digital inputs Number of digital inputs ● of which inputs usable for technological functions 4; HSC (High Speed Counting) 	
Digital inputs Number of digital inputs 8; Integrated ● of which inputs usable for technological functions 4; HSC (High Speed Counting)	
Number of digital inputs ● of which inputs usable for technological functions 8; Integrated 4; HSC (High Speed Counting)	
• of which inputs usable for technological functions 4; HSC (High Speed Counting)	
Number of simultaneously controllable inputs	
all mounting positions	
— up to 40 °C, max.	
Input voltage	
• Rated value (DC) 24 V	
• for signal "0" 5 V DC at 1 mA	
• for signal "1" 15 V DC at 1 mA	
Input delay (for rated value of input voltage)	
for standard inputs	
— parameterizable 0.2 ms, 0.4 ms, 0.8 ms, 1.6 ms, 3.2 ms, 6.4 ms and 12.8 ms, sel	ectable
in groups of four	
— at "0" to "1", min. 0.2 ms	
— at "0" to "1", max. 12.8 ms	
for interrupt inputs	
— parameterizable Yes	
for technological functions	
— parameterizable Single phase: 3 @ 100 kHz & 3 @ 30 kHz, differential: 3 @ 80 kl @ 30 kHz	Hz & 3
Cable length	
• shielded, max. 500 m; 50 m for technological functions	
• unshielded, max. 300 m; for technological functions: No	
Digital outputs	
Number of digital outputs 6	
• of which high-speed outputs 4; 100 kHz Pulse Train Output	
Limitation of inductive shutdown voltage to L+ (-48 V)	
Switching capacity of the outputs	
• with resistive load, max. 0.5 A	
• on lamp load, max. 5 W	

Output voltage	0.4 \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
• for signal "0", max.	0.1 V; with 10 kOhm load
• for signal "1", min.	20 V
Output current	
• for signal "1" rated value	0.5 A
for signal "0" residual current, max.	0.1 mA
Output delay with resistive load	
• "0" to "1", max.	1 µs
• "1" to "0", max.	5 μs
Switching frequency	400 1415
of the pulse outputs, with resistive load, max.	100 kHz
Relay outputs	
Number of relay outputs Cable langth	0
Cable length	500
• shielded, max.	500 m
• unshielded, max.	150 m
Analog inputs	
Number of analog inputs	2
Input ranges	Y.
• Voltage	Yes
Input ranges (rated values), voltages	Y.
• 0 to +10 V	Yes
— Input resistance (0 to 10 V)	≥100k ohms
Cable length	400 4 14 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1
• shielded, max.	100 m; twisted and shielded
Analog outputs	
Number of analog outputs	0
Analog value generation for the inputs	
Integration and conversion time/resolution per channel	
 Resolution with overrange (bit including sign), max. 	10 bit
 Integration time, parameterizable 	Yes
Conversion time (per channel)	625 µs
Encoder	
Connectable encoders	
• 2-wire sensor	Yes
1. Interface	
Interface type	PROFINET
Isolated	Yes
automatic detection of transmission rate	Yes
Autonegotiation	Yes
Autocrossing	Yes
Interface types	
RJ 45 (Ethernet)	Yes
 Number of ports 	1
integrated switch	No
Protocols	
PROFINET IO Controller	Yes
PROFINET IO Device	Yes
 SIMATIC communication 	Yes
Open IE communication	Yes; Optionally also encrypted
Web server	Yes
Media redundancy	No
PROFINET IO Controller	
Transmission rate, max.	100 Mbit/s
Services	
— PG/OP communication	Yes; encryption with TLS V1.3 pre-selected
— Isochronous mode	No
— IRT	No
— PROFlenergy	No

Delayitimed -tture	Van
— Prioritized startup	Yes
 Number of IO devices with prioritized startup, max. 	16
Number of connectable IO Devices, max.	16
Number of connectable IO Devices, max. Number of connectable IO Devices for RT.	16
max.	10
— of which in line, max.	16
 Activation/deactivation of IO Devices 	Yes
 Number of IO Devices that can be 	8
simultaneously activated/deactivated, max.	
 Updating time 	The minimum value of the update time also depends on the
	communication component set for PROFINET IO, on the number of IO devices and the quantity of configured user data.
PROFINET IO Device	devices and the quantity of configured user data.
Services	
— PG/OP communication	Yes; encryption with TLS V1.3 pre-selected
— Isochronous mode	No
— IRT	No
— PROFlenergy	Yes
— Shared device	Yes
Number of IO Controllers with shared device.	2
max.	<u>-</u>
Protocols	
Supports protocol for PROFINET IO	Yes
PROFIsafe	Yes
PROFIBUS	Yes; CM 1243-5 (master) or CM 1242-5 (slave) required
OPC UA	Yes; OPC UA Server
AS-Interface	Yes; CM 1243-2 required
Protocols (Ethernet)	
• TCP/IP	Yes
• DHCP	No
• SNMP	Yes
• DCP	Yes
• LLDP	Yes
Redundancy mode	
Media redundancy	
— MRP	No
— MRPD	No
Open IE communication	
• TCP/IP	Yes
— Data length, max.	8 kbyte
• ISO-on-TCP (RFC1006)	Yes
— Data length, max.	8 kbyte
• UDP	Yes
— Data length, max.	1 472 byte
Web server	
• supported	Yes
User-defined websites	Yes
OPC UA	
Runtime license required	Yes; "Basic" license required
OPC UA Server	Yes; data access (read, write, subscribe), method call, runtime license
-	required
 Application authentication 	Available security policies: None, Basic128Rsa15, Basic256Rsa15, Basic256Sha256
 User authentication 	"anonymous" or by user name & password
 Number of sessions, max. 	10
 Number of subscriptions per session, max. 	50
— Sampling interval, min.	100 ms
— Publishing interval, min.	200 ms
Number of server methods, max.	20
 Number of monitored items, max. 	1 000
Number of server interfaces, max.	2

 Number of nodes for user-defined server 	2 000
interfaces, max.	
Further protocols	Voc
MODBUS communication functions / boader	Yes
communication functions / header	
S7 communication	V
• supported	Yes
• as server	Yes
as client	Yes
User data per job, max. Number of connections	See online help (S7 communication, user data size)
overall	PG Connections: 4 reserved / 4 max; HMI Connections: 12 reserved / 18 max; S7 Connections: 8 reserved / 14 max; Open User Connections: 8 reserved / 14 max; Web Connections: 2 reserved / 30 max; OPC UA Connections: 0 reserved / 10 max; Total Connections: 34 reserved / 64 max
Test commissioning functions	
Status/control	
Status/control variable	Yes
Variables	inputs/outputs, bit memories, DBs, peripheral I/Os (without fail-safe), times, counters
Forcing	
Forcing	Yes; peripheral inputs/outputs (without fail-safe)
Diagnostic buffer	
• present	Yes
Traces	
Number of configurable Traces	2
Memory size per trace, max.	512 kbyte
Interrupts/diagnostics/status information	
Diagnostics indication LED	
RUN/STOP LED	Yes
• ERROR LED	Yes
MAINT LED	Yes
Integrated Functions	
Counter	6
Number of counters Counting frequency, may	6
Counting frequency, max.	100 kHz
Frequency measurement	Yes
Controlled positioning	Yes 8
Number of position-controlled positioning axes, max. Number of positioning axes via pulse-direction interface	
PID controller	Up to 4 with SB 1222 Yes
Number of alarm inputs	4
Number of pulse outputs	4
Limit frequency (pulse)	100 kHz
Potential separation	100 M IZ
Potential separation digital inputs • Potential separation digital inputs	No
between the channels, in groups of	1
Potential separation digital outputs	
Potential separation digital outputs	Yes
between the channels	No
between the channels, in groups of	1
EMC	
Interference immunity against discharge of static electricity	
Interference immunity against discharge of static	Yes
electricity acc. to IEC 61000-4-2	, 55
Test voltage at air discharge	8 kV
Test voltage at contact discharge	6 kV
Interference immunity to cable-borne interference	
 Interference immunity on supply lines acc. to IEC 	Yes

61000-4-4	
• Interference immunity on signal cables acc. to IEC 61000-4-4	Yes
Interference immunity against voltage surge	
 Interference immunity on supply lines acc. to IEC 61000-4-5 	Yes
Interference immunity against conducted variable disturban	ce induced by high-frequency fields
 Interference immunity against high-frequency radiation acc. to IEC 61000-4-6 	Yes
Emission of radio interference acc. to EN 55 011	
 Limit class A, for use in industrial areas 	Yes; Group 1
• Limit class B, for use in residential areas	Yes; When appropriate measures are used to ensure compliance with the limits for Class B according to EN 55011
Degree and class of protection	
IP degree of protection	IP20
Standards, approvals, certificates	
CE mark	Yes
UL approval	Yes
cULus	Yes
FM approval	Yes
RCM (formerly C-TICK)	Yes
KC approval	Yes
Marine approval	Yes
Highest safety class achievable in safety mode	165
Performance level according to ISO 13849-1	PLe
SIL acc. to IEC 61508	SIL 3
	SIL 3
Ambient conditions	
Free fall	
Fall height, max.	0.3 m; five times, in product package
Ambient temperature during operation	
• min.	0 °C
● max.	55 °C; Number of simultaneously activated inputs or outputs 4 or 3 (no adjacent points) at 60 °C horizontal or 50 °C vertical, 8 or 6 at 55 °C horizontal or 45 °C vertical
 horizontal installation, min. 	0°C
 horizontal installation, max. 	55 °C
 vertical installation, min. 	0°C
 vertical installation, max. 	45 °C
Ambient temperature during storage/transportation	
• min.	-40 °C
• max.	70 °C
Air pressure acc. to IEC 60068-2-13	
Operation, min.	795 hPa
Operation, max.	1 080 hPa
Storage/transport, min.	660 hPa
Storage/transport, max.	1 080 hPa
Altitude during operation relating to sea level	. 000 III W
Installation altitude, min.	-1 000 m
Installation altitude, min. Installation altitude, max.	5 000 m; Restrictions for installation altitudes > 2 000 m, see manual
Relative humidity	5 555 III, Restrictions for installation attitudes > 2 500 III, See IIIdiludi
-	95 %; no condensation
Operation, max. Vibrations	55 70, NO CONCENSATION
Vibrations	2 g (m/s²) wall mounting, 1 g (m/s²) DIN rail
 Operation, tested according to IEC 60068-2-6 	Yes
Shock testing	100
• tested according to IEC 60068-2-27	Yes; IEC 68, Part 2-27 half-sine: strength of the shock 15 g (peak value), duration 11 ms
-	
tested according to IEC 60068-2-27 Pollutant concentrations	value), duration 11 ms
• tested according to IEC 60068-2-27	

configuration / programming / header	
Programming language	
— LAD	Yes; incl. failsafe
— FBD	Yes; incl. failsafe
— SCL	Yes
Know-how protection	
 User program protection/password protection 	Yes
 Copy protection 	Yes
Block protection	Yes
Access protection	
 protection of confidential configuration data 	Yes
 Protection level: Write protection 	Yes
 Protection level: Read/write protection 	Yes
Protection level: Complete protection	Yes
programming / cycle time monitoring / header	
adjustable	Yes
Dimensions	
Width	90 mm
Height	100 mm
Depth	75 mm
Weights	
Weight, approx.	370 g
last modified:	5/17/2021 🖸